## CODISPLAY: BCD PARALLEL

Art. No. 190001
DIP seLectable as either 4 or 6 displayed LED digits

## 14mm 7-SeGment LEDs

Characters"0123456789AbCd-"
Red contrast filter
BCD interface (parallel)
Panel mounting

## Front IP65 PRotected


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## 1．DIP SWITCH SETTINGS：

SW1：FUNCTIONAL DIP SWITCH
SW1－1，SW1－2，SW1－3
LED brightness control．

| Brightness |  | SW1 | SW1－2 | SW1－3 |
| :---: | :---: | :---: | :---: | :---: |
| $2 \%$ | lowest | OFF | OFF | OFF |
| $10 \%$ |  | ON | OFF | OFF |
| $15 \%$ |  | OFF | ON | OFF |
| $25 \%$ |  | ON | ON | OFF |
| $35 \%$ |  | OFF | OFF | ON |
| $50 \%$ |  | ON | OFF | ON |
| $75 \%$ |  | ON | ON | OFF |
| $98 \%$ | highest | ON | ON | ON |
|  |  |  |  |  |

SW1－4－Entry mode．
When is OFF Indicators is addressed from left to right．
When is ON Indicators is addressed from right to left．

## SW1－5

4 or 6 displayed LED digits．
ON： 4 displayed digits
OFF： 6 displayed digits．

## SW1－6

Display initial setting
ON：All digits display 0 after power up．
OFF：Blank display after power up．
Note：The DIP Switch settings must only be changed when the power to the device is turned off．The DIP Switch settings come into effect on power up．

## 2. INDICATOR NUMBERING

The following figure shows the numbering of indicators from left to right.


Figure 1

## 3. PARAMETRIC BYTE

The data at the input are displayed in decimal format. This requires the CS signal (Chip Select) to be activated. As soon as the data and the address signals are stable, the read and display cycle is triggered by the rising edge of the RS (Register Select) signal. Refer to figure 2. The new data appears on display when both signals, CS and RS, are low.


Figure 2
The Parametric Byte contains the information about the decimal point, the address and data. Figure 3 shows the structure of the Parametric Byte.


Figure 3
DECIMAL POINT: The decimal point is set with bit 3 of the upper nibble of the Parametric Byte.

ADDRESS RANGE: Each Indicator is addressed via Bits 4,5 and 6 of the upper nibble of the Parametric Byte.The address can be in the range from Ohex (1stindicator depending of position of SW1-4) to 5hex (last indicator depending of position of SW1-4) for 6 displayed digits. In the 4 digit mode the address range is from Ohex (indicator 1) to 3hex (indicator 4).

DATA: The data can be in range from Ohex to Fhex and occupies the lower nibble of Parametric Byte. Figure 4 shows all combinations from Ohex to Fhex.


Figure 4

## Data Bit Codes:

| Data bit 0 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Data bit 1 | 0 | 0 | 1 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 1 | 1 |
| Data bit 2 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 |
| Data bit 3 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| Hex. | $0 h$ | 1 h | 2 h | 3 h | 4 h | 5 h | 6 h | 7 h | 8 h | 9 h | Ah | Bh | Ch | Dh | Eh | Fh |

Table 3
Character displayed:


Figure 5

## DATA AND COMMAND SEQUENCE:

Each digit has to be set separately with the following sequence:
a. Apply signals Data at the inputs D 0 to D 3 , the address at ADO to $\mathrm{AD2}$ and the decimal point at $A D 3$.
b. Hold signals D and AD stable and apply the control signals CS and RS as described in figure 2.

Repeat this sequence for each digit.

## EXAMPLE 1:

Display "1234.56" in 6 digit mode:
Set SW1-4 - OFF - set entry mode left to right
Set SW1-5 - OFF - set 6 digit mode
Set SW1-6 - ON - All digits display 0 after power up.
Turn power on.

- Apply sequence: 01
- apply control signals CS and RS as described in figure 2.
- Apply sequence: 12
- apply control signals CS and RS as described in figure 2.
- Apply sequence: 23
- apply control signals CS and RS as described in figure 2.
- Apply sequence: B4
- apply control signals CS and RS as described in figure 2.
- Apply sequence: 45
- apply control signals CS and RS as described in figure 2.
- Apply sequence: 56
- apply control signals CS and RS as described in figure 2.

If the SW1-4 is se to ON Codisplay will point 654.321

## 4. SIGNAL INPUTS

| Terminal <br> X3/ | Symbol | Description | Signal |
| :---: | :--- | :--- | :--- |
| 1 | GND | Signal Earth | 0 VDC |
| 2 | D0 | data bit 0 | 24 VDC |
| 3 | D1 | data bit 1 | 24 VDC |
| 4 | D2 | data bit 2 | 24 VDC |
| 5 | D 3 | data bit 3 | 24 VDC |
| 6 | AD0 | address bit 0 | 24 VDC |
| 7 | AD1 | address bit 1 | 24 VDC |
| 8 | AD2 | address bit 2 | 24 VDC |
| 9 | AD3 | decimal point | 24 VDC |
| 10 | R/W | Read/write signal | 24 VDC |
| 11 | CS | Chip select signal | 24 VDC |
| 12 | RS | Register select signal | 24 VDC |

Table 5

## X3/1 to X3/12:

Spring terminal connection for conductors with cross section of $0.14 \mathrm{~mm}^{2}$ to $0.5 \mathrm{~mm}^{2}$ / AWG 26 to AWG 20
NOTE: The presentation of a signal >16 VDC on any of the signal terminals is converted to a binary 1 (ON), signals of $<3$ VDC are converted to binary 0 (OFF).

## 5. POWER SUPPLY

The power supply must be in range form 16 VDC up to $30 \mathrm{VDC} / 1 \mathrm{~A}$. Codisplay is protected against improper power supply connection.
$\mathrm{X} 1 / 1=$ Vin + ve of DC source ( square pad ) marked on PCB as "+8..24V" X1/2 = GND -ve of DC source ( round pad ) marked on PCB as "GND"
$\mathrm{X} 1 / 1$ and $\mathrm{X} 1 / 2$ :
Spring terminal connection for conductors with cross section of $0.14 \mathrm{~mm}^{2}$ to $0.5 \mathrm{~mm}^{2}$ / AWG 26 to AWG 20

| Power supply consumption Codisplay |  |  |  |
| :---: | :---: | :---: | :---: |
|  | PWM 0\% | PWM 50\% | PWM 98\% |
| Voltage 24V | 15 mA | 38 mA | 60 mA |

Table 6
Measurements were made with all LED segments and decimal points switched on.

## 6. INDICATOR TEST

The Indicator test can be performed at any time using jumper JP3.

## 7. CE CONFORMITY AND ROHS COMPLIANCE

Crameda AG declares that Codisplay 190001 complies with the requirements for CE conformity. However this can only be maintained by complying with the following installation instructions:

1. The signal cable must not exceed 1 metre.
2. The signal cable must be supplied via a current compensating single-aperture core filter to the Codisplay. Signal lines must be wound with the same alignment (angular) with at least 2 windings on the filter.

All PCB boards, components and solder paste are manufactured with leadless technology and meet the requirements for RoHS Environment friendly fabrication.

## 8. PANEL MOUNTING

## PANEL CUTOUT:



| Millimetres | Inches |
| :---: | :---: |
| 92.5 | 3.642 |
| 21.5 | 0.846 |

## DIMENSIONS:



| Dimension | Description | Millimetres | Inches |
| :---: | :---: | :---: | :---: |
| A | Height | 24 | 0.945 |
| B | Depth | 108 | 4.252 |
| C | Width | 96 | 3.780 |
| D | LED | 14 | 0.551 |

Weight approx. 130 grams

Specifications are subject to change without notice.

## A. EXAMPLES

## EXAMPLE 1 IN DETAIL:

Display " 1234.56 " in 6 digit mode:

- Turn power off, set SW1-1 to SW1-3 to ON, SW1-4 to SW1-6 to OFF, turn power on.
- Apply sequence: 01 "\%" 12 "\%" 23 " \%" B4 "\%" 45 "\%" 56 " \%" at the inputs.
"\%" = apply control signals CS and RS as described in the figure 2.
For details see table 4

$0=$ signal of $<8 \mathrm{VDC}$
1 = signal of $>12 \mathrm{VDC}$
"\%" = apply control signals CS and RS as described in the figure 2
Table 4


## EXAMPLE 2:

Change Display " 1234.56 " to "123.-- " in 6 digit mode:

- For CODISPLAY 190001 apply sequence: A3 "\%" 3E "\%" 4E "\%" 5F "\%" at the inputs.
- For CODISPLAY 190001B apply sequence: A3 " $\%$ " 3 A " $\%$ " 4 A " $\%$ " 5 F " $\%$ " at the inputs.


## EXAMPLE 3:

Display " -1.09 " in 4 digit mode:

- Turn power off, set SW1-1 to SW1-3 and SW1-5 to ON, SW1-4 and SW1-6 to OFF, turn power on.
- For CODISPLAY 190001 apply sequence: OE "\%" 91 "\%" 20 "\%" 39 "\%" at the inputs.
- For CODISPLAY 190001B apply sequence: OA "\%" 91 "\%" 20 "\%" 39 "\%" at the inputs.

